ABSTRACT

The invention involves an apparatus (2) for correction of a resampler with which a sampled input signal (S_D), that is subjected to an input sampling rate (f_A) and which has a chip frequency (f_C) that differs from the input sampling rate (f_A), is converted into a sampled output signal (S_C) for which the sampling rate corresponds with the chip frequency (S_C), by changing the input sampling rate (f_A) by a resampling factor. The apparatus (2) includes a non-linear element (8) that subjects the input signal (S_D) to a non-linear operation so that a spectral line (15) is produced at the chip frequency (f_C) and a frequency shifter (9) that spectrally shifts the input signal (S_D) by the chip frequency (f_C). Further, a phase determining device (11) determines the phase of the shifted spectral line at the chip frequency (f_C) as a function of sampling time points. A regression and correcting device (13) corrects on a basis of the regression of the phase of the shifted spectral line at the chip frequency (f_C) the resampling factor and/or time-wise shifts the output signal (S_C) by a time correction value.